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PhaseLink Corporation Announces the Newest in its PicoPLL Programmable PLL Clock Family

Three Programmable PLLs, 8 LVCMOS Clock Outputs, 1.8V~3.3V Operation, Lowest Power, With Smallest Footprint

Fremont, California. – October 27th 2008 – **For Immediate Release**



PhaseLink Corporation, a leader in frequency source generation and the inventor of the Analog Frequency Multiplier (industry's best performing clock IC), and PicoPLL (world's smallest and lowest power programmable clocks) today announced the availability of the newest member of PhaseLink's extensive PicoPLL clock family, PL613-XX.

PhaseLink's PL613 family is the world's lowest power, smallest footprint, and best performing 3-PLL programmable clock. This family offers up to 200MHz output from a reference or low-frequency crystal input, low period jitter, eight (8) unique clock outputs with optional individual OE or a Master OE, Power down option feature that consumes <math><10\mu\text{A}</math>, when activated, and three (3) output buffer drives to select from to accommodate high frequency signal requirements. In addition, each IC can be pre-programmed to allow for cycling through four (4) user-defined programmed configurations or just switching the frequency on a single output pin only, to meet performance requirements of various system level industry standards. The PL613 family is housed in a 10-pin MSOP, 16-pin QFN, 8-pin SOP, or 16-pin (T)SSOP **GREEN/RoHS** compliant packages.

"PL613 family is specifically designed to save 70%-80% board space and reduce the implementation cost by 40%-60%, when compared to conventional clocking methods.", Said Amir Naghavi, VP of Marketing for PhaseLink Corporation. "The PL613 family of programmable clocks is ideal for designs that demand high performance while requiring low-power, small footprint, and the flexibility to meet the industry standards' system design requirements."

PL613 family is ideal for applications such as Triple Play Modem, Personal Navigation Devices (PND), FemtoCells, Digital Televisions (DTV), Multi-Function Printers (MFP), consumer electronics/portable devices, Routers and other communication products, and any application where performance, power, space savings, and cost are important.

With a flexible input (Crystal input from 10MHz to 50MHz, Reference Clock input from 5MHz to 200MHz) and outputs from 1MHz to 200MHz, the PL613 family has the configurability to meet the most demanding timing requirements. The PL613 has three programmable PLLs and up to 8 LVCMOS outputs all housed in a small 3 x 3mm 16-pin QFN GREEN/RoHS package. The flexible architecture of the PL613 includes four pins that are able to be configured as Output Enable (OE) or Clock Outputs allowing for configurations with four distinct frequencies, each having individual Output Enable control. One pin can also be programmed as a master Power Down pin (PDB) that puts the device into Sleep Mode with <math><10\mu\text{A}</math> of current consumption.

Product Family Summary:

Part Number	Input (MHz)	Output (MHz)	Operating Voltage	Features	Package
PL613-01	<ul style="list-style-type: none"> Fundamental: 10 – 50 Reference: 5-200 	<ul style="list-style-type: none"> ≤133MHz @ 1.8V operation ≤166MHz @ 2.5V operation ≤200MHz @ 3.3V operation 	1.8V* 2.5V 3.3V	<ul style="list-style-type: none"> Up to 8 programmable clocks 4 on-the-fly configuration switching Programmable OE, Master-OE, PDB, CSEL*, CLK 3-Output drive strength select Frequency switching on a single output 	<ul style="list-style-type: none"> 3x3 QFN-16L (T)SSOP-16-L
PL613-05	<ul style="list-style-type: none"> Fundamental: 10 – 50 Reference: 5-200 	<ul style="list-style-type: none"> ≤133MHz @ 1.8V operation ≤166MHz @ 2.5V operation ≤200MHz @ 3.3V operation 	1.8V* 2.5V 3.3V	<ul style="list-style-type: none"> Up to 8 programmable clocks Programmable Master-OE, PDB, CLK1 3-Output drive strength select 	<ul style="list-style-type: none"> SOP-8L MSOP-10L

* Samples will be available in early 1Q09

Pricing and Availability

The PL613 is available for sampling and production use. Offered in **GREEN/RoHS** compliant TSSOP-16 and QFN-16 (3 x 3mm) packages, the PL613 is priced at \$1.50 in moderate quantities.

About PhaseLink Corporation

PhaseLink Corporation is the recognized leader in the mixed signal technology for timing source and signal conditioning ICs, in the area of Communications, Data Storage, Personal Computing, and Consumer Products. An innovator of many first to market products such as Analog Frequency Multipliers, Triangular Modulation SST, PicoPLL (world's smallest programmable clock), PhaseLink is principally focused on providing high performance Frequency timing (XO, VCXO, EMI Reduction, Programmable Clocks, ZDBs, etc.) clock IC solutions.

Additional information and datasheets can be found at www.phaselink.com or by calling 510-492-0990.

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