

PhaseLink Corporation Announces the Availability of PicoPLL™; World's Smallest, best performing, and Lowest-Power Programmable Clock
1.8V~3.3V Operation, GREEN/RoHS Compliant Packages and the Coolest Operation

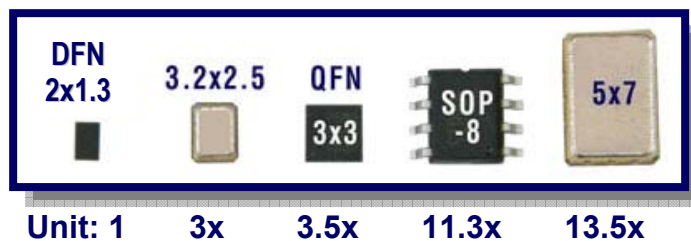
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Up to 200MHz frequency with exceptional Jitter and Phase Noise performance, small footprint (DFN:2.0x1.3x0.6mm) **GREEN**/RoHS compliant packaging, lowest power consumption, and near 0ppm frequency tuning are a few attributes of PhaseLink's highly featured PicoPLL.

PhaseLink Corporation, a leader in frequency source generation and the inventor of the Analog Frequency Multiplier™, and PhasorV™ today announced the availability of its '**Ultra-Low-power**', Smallest package, High performance PicoPLL clock family. PicoPLL is the newest member of PhaseLink's programmable clocks.

"Developing breakthrough products is something our customers have become accustomed to expect from PhaseLink", said Amir Naghavi, VP of Marketing for PhaseLink Corporation. "Offering best of class performance and 0ppm frequency tuning, along with the lowest power consumption, smallest package footprint, and host of other features clearly makes PicoPLL another technology breakthrough from PhaseLink."

The PicoPLL family is targeted for performance driven designs, in particular where space and power constraints are essential. PicoPLL family encompasses a broad line of programmable clocks that are easily configurable to the requirements of most applications offering the lowest power consumption, and jitter and phase noise performance. The PicoPLL family is housed in the smallest of **GREEN**/RoHS compliant packages (DFN: 2.0mmx1.3mmx0.6mm, SC70: 2.3mmx2.25mm, SOT23: 3mmx3mm, or even in a 2.5mmx2.0mm oscillator module), supporting commercial (0° C to +70° C) and industrial (-40° C to +85° C) temperature ranges. In addition, all PicoPLL family members operate on 1.8V, 2.5V, or 3.3V ± 10% power supply.



PicoPLL can be programmed to generate up to 200MHz from a low-cost 10MHz-50MHz fundamental crystal, or accept a reference input from 10KHz-200MHz and is capable of consuming less than 10µA of power (when PDB is activated) with the lowest peak to peak jitter and phase noise performance.

Product Summary:

Part Number	Input (MHz)	Output (MHz)	Operating Voltage	Features	Package
PL611s-02	<ul style="list-style-type: none"> Fundamental: 10 - 50 Reference : <200 	<200	1.8V 2.5V 3.3V	<ul style="list-style-type: none"> Up to 2 programmable clocks. Programmable FSEL, PDB, or OE 	Die, Wafer DFN-6 SC70-6 SOT23-6
PL611s-28	<ul style="list-style-type: none"> Fundamental: 10 - 50 Reference : <54 	<54	1.8V 2.5V 3.3V	<ul style="list-style-type: none"> Specially designed for Ultra low-power Up to 2 programmable clocks. Programmable FSEL, PDB, or OE 	DFN-6 SC70-6 SOT23-6
PL611s-15	<ul style="list-style-type: none"> Reference input * 10KHz ~ 200MHz 	<200	1.8V 2.5V 3.3V	<ul style="list-style-type: none"> 1 programmable output Converts KHz input to MHz output 	DFN-6 SC70-6 SOT23-6
PL611s-16	<ul style="list-style-type: none"> Reference input * 10KHz ~ 200MHz 	<54	1.8V 2.5V 3.3V	<ul style="list-style-type: none"> Specially designed for Ultra low-power 1 programmable output Converts KHz input to MHz output 	DFN-6 SC70-6 SOT23-6
PL611s-26	<ul style="list-style-type: none"> Reference input * 1.0MHz ~ 200MHz 	<200	1.8V 2.5V 3.3V	<ul style="list-style-type: none"> Up to 2 programmable clocks Both, FSEL & PDB, or FSEL & OE can be available in one clock design 	DFN-6 SC70-6 SOT23-6
PL611s-27	<ul style="list-style-type: none"> Reference input * 10KHz ~ 200MHz 	<54	1.8V 2.5V 3.3V	<ul style="list-style-type: none"> Up to 2 outputs Programmable PDB, FSEL, OE 	DFN-6 SC70-6 SOT23-6

For additional information on PicoPLL product family, or to request evaluation samples, demo boards, and Gerber files for optimum placement of PicoPLL, please visit [<http://www.phaselink.com/>].

Pricing and Availability

The PicoPLL is currently available in **GREEN**/RoHS compliant DFN, SC70, and SOT23 packages and is priced at \$0.48 in moderate quantities.

About PhaseLink Corporation

PhaseLink Corporation is the recognized leader in the mixed signal technology for timing source and signal conditioning ICs, in the area of Communications, Data Storage, Personal Computing, and Consumer Products. An innovator of many first to market products such as Analog Frequency Multipliers, Triangular Modulation SST, Bi-directional I/O latched Clock chip, SMART-BYTE™ Programmable clock chip, etc., PhaseLink offers one of the broadest IC product lines in the industry (XO, VCXO, AFM, PhasorV, EMI reduction, programmable, Video, and LAN clocks).

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