

PhaseLink Corporation Announces Advanced Fanout and Oscillator Buffers with 2 to 6 Outputs (for use with Crystal or Reference Clock Inputs)

1MHz to 150MHz Input and Output, 1.62V~3.63V operation, Lowest Power Consumption, With Smallest Footprint

Fremont, California. – March 21, 2011 – For Immediate Release

PhaseLink Corporation, a leader in frequency source generation and the inventor of Analog Frequency Multiplier (industry’s best performing XO and VCXO clock multiplier ICs), and PicoPLL (world’s smallest and lowest power programmable clocks) today announced the availability of a new family of fanout buffer families, PL133-XX and PL135-XX.



These new PL133 and PL135 line of Fanout buffers include PhaseLink’s competencies of high performance, lowest power consumption, smallest footprints (2.0x1.3x0.6mm DFN package), extremely low additive jitter, and wide power supply range from 1.62V to 3.63V, while operating up to 150 MHz, and featuring the lowest output-to-output skew. The family of products accept inputs with <0.9V voltage swing with 2 to 6 outputs, while the 2 output device is capable of accepting a sinewave, duplicating the TCXO signal.

“PhaseLink’s introduction of PL133 and PL135 families is indicative of our commitment to being the leader in the timing source market with one of the most complete product lines.”, Said Amir Naghavi, VP of Marketing for PhaseLink Corporation. “These broadest and best performing products cover a wide range of applications from handheld, mobile, and consumer products to high performance communication products.”

With a flexible Crystal input from 10MHz to 40MHz, or Reference Clock input from 1MHz to 150MHz, lowest additive jitter, lowest output to output skew, lowest power consumption, and smallest foot print, the PL133-XX and PL135-XX families have the attributes of meeting the most demanding timing requirements. These families of products are designed to support a wide range of applications from handhelds and mobile devices to consumer and communications products.

PhaseLink’s PL133-XX (Reference Input) Product Family Summary:

Part Number	Input (MHz)	# of Output	Operating Voltage	Features	Package
PL133-67	• Ref. CLK: DC to 150	6	2.25V to 3.63V	<ul style="list-style-type: none"> Additive Jitter: <60fs Output to Output Skew < 250ps 6 Outputs with OE Control 	• TSSOP-16L
PL133-47	• Ref. CLK: DC to 150	4	2.25V to 3.63V	<ul style="list-style-type: none"> Additive Jitter: <60fs Output to Output Skew < 250ps 4 Outputs 	• SOP-8L
PL133-37	• Ref. CLK: 1 to 150	3	1.62V to 3.63V	<ul style="list-style-type: none"> Additive Jitter: <70fs Output to Output Skew 250ps 3 Outputs with 1 Master OE for all outputs 	• SOT-6L
PL123-27	• Ref. CLK: 1 to 150	2	1.62V to 3.63V	<ul style="list-style-type: none"> Additive Jitter: <130fs Output to Output Skew < 500ps Accepts sine-wave or LVCMOS input 	• DFN-6L

PhaseLink's PL135-XX (Crystal Input) Product Family Summary:

Part Number	Input (MHz)	# of Output	Operating Voltage	Features	Package
PL135-67	• Crystal: 10 to 40	6	1.62V to 3.63V	<ul style="list-style-type: none"> • Output to Output Skew < 1ns • 6 Outputs with 1 Master OE for all outputs 	<ul style="list-style-type: none"> • TSSOP-16L • QFN-16L
PL135-47	• Crystal: 10 to 40	4	1.62V to 3.63V	<ul style="list-style-type: none"> • Output to Output Skew < 1ns • 4 Outputs with 2 OE Controls 	<ul style="list-style-type: none"> • TSSOP-16L • QFN-16L
PL135-37	• Crystal: 10 to 40	3	1.62V to 3.63V	<ul style="list-style-type: none"> • Output to Output Skew < 250ps • 3 Outputs with 1 Master OE for all outputs 	<ul style="list-style-type: none"> • SOP-8L
PL135-27	• Crystal: 10 to 40	2	1.62V to 3.63V	<ul style="list-style-type: none"> • Output to Output Skew < 500ps 	<ul style="list-style-type: none"> • DFN-6L

Pricing and Availability

The PL133-XX and PL135-XX families are available for sampling and production, and are offered in GREEN/RoHS compliant packages. They're priced at \$0.50 (output dependent) in moderate quantities.

About PhaseLink Corporation

PhaseLink Corporation is the recognized leader in the mixed signal technology for timing source and signal conditioning ICs, in the area of Communications, Data Storage, Personal Computing, and Consumer Products. An innovator of many first to market products such as Analog Frequency Multipliers, Triangular Modulation SST, PicoPLL (world's smallest programmable clock), PhaseLink is principally focused on providing high performance Frequency timing (XO, VCXO, EMI Reduction, Programmable Clocks, ZDBs, etc.) clock IC solutions.

Additional information and datasheets can be found at www.phaselink.com or by calling 408-571-1668.

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