

PhaseLink Corporation Announces the Highest Performance, Smallest Footprint Programmable Clocks with Pb (lead)-Free Packaging

Fremont, California. – January 28, 2005 -- PhaseLink Corporation, a leader in frequency source generation and the inventor of the Analog Frequency Multiplier™, today announced the availability of its new Quick Turn Clock™ (QTC) family of programmable clock solutions. The QTC family includes a broad line of Programmable clocks that are easily configurable to the requirements of most applications, offer the lowest jitter and phase noise performance, and are housed in the smallest package footprint (SOT23-6L 3mmx3mm) of any programmable device in the market.

“Almost every design block is programmable to allow for quick fine-tuning of the IC to the customer requirements,” said Pierre Guebels, VP Engineering, PhaseLink Corporation. “The programming sophistication is matched only with easy to use programming software that requires no PLL design knowledge or expertise.”

With up to 375 Mhz differential LVPECL, LVDS, or single ended CMOS, the QTC family is designed to eliminate the need for expensive custom or high frequency oscillators or multiple crystals. This product family is specifically designed to save board space and implementation cost, while offering multiple programming options. A list of available options and their unique features are:

Part Number	Input (MHz)	Output (MHz)	Features	Package	Price
PL611-01	<ul style="list-style-type: none"> ▪ Fundamental: 10 - 30 ▪ 3rd overtone: <75 ▪ Reference : <200 	<200	<ul style="list-style-type: none"> ▪ Up to 3 programmable clocks. ▪ Frequency Select input, PDB, or OE ▪ 2.5V~3.3V 	MSOP-8 SOIC-8 SOT23-6	< \$1.00 in volume
PL611-05	<ul style="list-style-type: none"> ▪ Fundamental: 10 - 30 ▪ Reference : <100 	<35	<ul style="list-style-type: none"> ▪ 2 Programmable Clock, or ▪ 1 Prog. CLK + 1 Reference Output ▪ 2.5V~3.3V 	SOT23-6	< \$0.25 in volume
PL611-07	<ul style="list-style-type: none"> ▪ Fundamental: 10 - 30 ▪ Reference : <100 	<75	<ul style="list-style-type: none"> ▪ 1 programmable clock. ▪ Frequency Select input ▪ 2.5V~3.3V 	SOT23-6	< \$0.40 in volume
PL611-20	<ul style="list-style-type: none"> ▪ Fundamental: 10 - 30 ▪ 3rd overtone: <75 	<200	<ul style="list-style-type: none"> ▪ 1 programmable dual output, 1 REF ▪ Frequency Select input ▪ 2.5V~3.3V 	MSOP-8 SOIC-8 SOT23-6	< \$0.75 in volume
PL611-30	<ul style="list-style-type: none"> ▪ Fundamental: 10 - 30 ▪ Reference : <200 	<375	<ul style="list-style-type: none"> ▪ 1 pair differential PECL, LVDS, or CMOS Output + 1 REF ▪ 3.3V 	MSOP-8 SOIC-8	< \$1.00 in volume

The QTC product family supports 10-30MHz fundamental or up to 75MHz 3rd overtone crystal, in addition to up to 200MHz reference clock input for cascading the ICs to produce additional clock frequencies required for the system implementation and further cost reduction.

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They are offered in 6-pin SOT23 or 8-pin MSOP Pb (lead)-free packages, and support commercial (0 to +70 degrees C) and industrial (-40 to +85 degrees C) temperature ranges.

About PhaseLink Corporation

PhaseLink Corporation is the recognized leader in the mixed signal technology for timing source and signal conditioning ICs, in the area of Communications, Data Storage, Personal Computing, and Consumer Products. An innovator of many first to market products such as Analog Frequency Multipliers, Triangular Modulation SST, Bi-directional I/O latched Clock chip, SMART-BYTE™ Programmable clock chip, etc.), PhaseLink offers one of the broadest IC product lines in the industry (XO, VCXO, Analog Frequency Multipliers, EMI reduction, programmable, Video, and LAN clocks).

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